POLITECNICO DI TORINO

Master's Degree in Electronic Engineering



Master's Degree Thesis

Capacitive Sensor Front-end Using Carrier Demodulation

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Summary

Nowadays, indoor human localization plays an important role in security system, smart house and health-care monitoring. It can be used to optimize the way to control the devices which require the locational information of a person. There are many technologies for indoor human localization have been developed in past decades. The application is usually based on WI-FI, infrared, RFID, bluetooth, ultrasonic sensors. Every technology has its own advantages and limitations.

Capacitive sensors are considered for indoor localization in this project due to their low cost, low power consumption and effective sensing. The applications based on capacitive sensors can be contactless and wear-free. It is desirable that the sensing ranges are much larger (10-20x) than the diameter of the sensing plate.

The purpose of this research project is to design a front-end interface for a long range, low cost indoor human localization with capacitive sensors. The existing design uses a carrier demodulation technique in which the phase and amplitude of the signal affected by the presence of the human body. The basic idea is to generate the carrier with sine wave of 10 kHz using the PWM block of ATmega328P microcontroller. The sine wave carrier passes a RC filter which is formed by the plate sensor and an adjustable resistor. The plate capacitance is affected by the distance between the person and the sensor plate. The change of the capacitance affects the phase and amplitude of the signal at the output of the RC filter. We convert both the RC sine wave signal and the original one into square waves using comparators, and compare their phases with an XOR gate. We filter the demodulated output and measure it using the built-in ADC of the microcontroller.

My contribution was to test the circuit, characterize the new front-end performance and design the PCB board of an improved version. For testing part, first I tested the sensitivity in a setting in which a person stand in front of the plate sensor at different ranges and was recorded the value from the microcontroller on a server through an XBEE radio module. Then, I tested the stability by running the circuit for hours in two ways. One was testing with a fixed capacitance capacitor instead of the plate sensor, and the other was testing with the plate sensor while no person stands in front of it. By performing these tests, a fourth order butterworth BP filter part was found to contribute drift noise, so it was removed.

Other problems were the large drift over time and the noise. Thus I did resoldering some parts of the circuit in a nicer way, changing some appropriate resistance or capacitance to several filters, and also replacing some components. Due to the change of some components, the power supply needed to be redesigned. For the analog part, the virtual ground needed to be precisely at the half of the analog voltage, thus a dual-output with Vref and Vref/2 outputs component is considered. For the digital part, a switching voltage regulator is needed. And to reduce the board size, a dual-out regulator is considered. After some tests for different voltage regulators, MAX6072 and LTC3256 were introduced, one for analog voltage and virtual ground, the other one for digital voltage and XBEE (3.3V). But cause of the components limitation, the needed power supply is about 5.5 V, so that an extra external boost regulator from 5 V was to be designed as a voltage adaptor.

In the end, I designed the PCB of the new front-end circuit. The basic rule was to design different parts of the circuit depending on whether they would affect each other, and to make the board size as small as possible. It was to reduce the noise from crosstalk. First I designed the power part to make sure that the power traces connect the components in short and in a tree shape. Then to design the XBEE and microcontroller parts with the power filtering parts close to the microcontroller. For the analog part, the different functional parts were designed with ground capacitors close to the power pins of the ICs, and all components were placed as close as possible. At the end, the design passed the DRC check.

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Chapter 1 Introduction

1.1 Indoor human localization

Nowadays technology especially the smart phones and other wireless devices expanding a huge influence of our daily life. In particular the "Smart Home", it provides us a convenient living environment. The need for indoor human localization is obvious in smart homes and assisted living environments. Reliable location information is vital for different location-based services that make living easier and safer. For example, one service that uses location information is the automatic adjustment of lighting, heating, or home electronic devices based on a user's physical location [1].

The positioning system should be extremely easy to use. In practice, the whole system should also be invisible to the user, as users might find even small sensors or actuators too unattractive if they are visible. [1]. Another concern is the privacy of people, unauthorized person should not be able to receive the localization information of the user. To be acceptable for home use, a positioning system needs to be passive, device-free, unobtrusive, and tag-less. Moreover, the power consumption of the system should be low for a long-term running and low maintenance.

According to the requirements, there are several types of technologies, each of them having its own advantages and limitations.

1. **Pressure Sensors** are the most traditional positioning system technology[1]. The pressure-sensitive sensors are installed in a group of load cells structure under the floor surface. They can identify a person corresponding to his unique weight and gait. The limitation is due to the high cost of their installation and maintenance. Also the system requires a lot of space which may not available to some situation.

- 2. Ultrasound mainly relies on ToF measurements of ultrasound signals (>20 kHz) and the sound velocity to calculate the distance between a transmitter and a receiver node [2]. Reflection of sound waves can be received and measured in time and frequency with precise calculation of position of the person. However, the ultrasound system requires numerous sensors to cover the house space. Moreover, it may cause some other side effects such as their possible harm for hearing and pets when it being an active system.
- 3. **Image Recognition** is to equip a set of sensors (cameras, Infrared thermal cameras) which remain at a fixed and known position in the location. This system is highly accurate and able to determine not only the position of person but his speed. Also it can identify the person. However, it has the drawback that it is not very well-suitable neither for privacy nor for power consumption. And it always requires a line of sight between the user and the camera.
- 4. **RFID** technology is primarily intended for transferring and storing data using electromagnetic transmission from a transmitter to any Radio Frequency (RF) compatible circuit [3].

Here we focus on **capacitive sensors** which are very suitable for mid-range positioning and tracking, they have low power consumption and low installation and maintenance cost. Also they are tag-less and no privacy problems. My task is to optimize and tune the capacitive sensor in terms of signal to noise ratio, power consumption, stability and maximum achievable detection range. And finally to design the sensor PCB.

1.2 Capacitive Sensing

Capacitive sensing is widely used on smart phones, tablets, and laptops from touchscreens and touchpads to the capacitive "buttons" as consumer electronics devices and commercial equipment. The use of capacitive sensing is common in human computer interaction research.

As shown in Figure 1.1, due to the capacitance between the human bodies and other objects, the information can be derived by measuring the level of the capacitance coupling that exist between conductive objects including people. We can consider the human body acts like a conductive plate of the parallel plate capacitor, with air as dielectric. Between the two conductive plates separated by a



Figure 1.1: Capacitive coupling in a room between different masses from [4].

dielectric, a voltage \mathbf{V} developed. From theory of capacitance, we know that

$$Q = C \cdot V \tag{1.1}$$

Where C is capacitance and Q is total charge held by plates. As mentioned in [5], the capacitance depending on its size, the distance to other objects, and the dielectric properties of the objects and the dielectric between them. When $A \gg d$,

$$Q = \frac{\varepsilon_0 \cdot k \cdot A}{d} \tag{1.2}$$

where C is the total capacitance formed between the two plates (in Farad), k is the relative dielectric permittivity of the material between plates (k = 1 in case of free space), 0 is the absolute dielectric permittivity of free space (8.854×10^{-12} F/m), and d is the distance between the capacitor plates (in meters). For our case, it is not valid because d»A for long range capacitive sensors. An approximated formula for the sensor is:

$$Q \cong \frac{k \cdot A}{d^{2 \sim 3}} \tag{1.3}$$



Figure 1.2: Capacitive coupling between sensor and objects from [6].

Referring to Figure 1.2 from [6], the capacitance is due to the four types of capacitances as follows:

- 1. Csb: capacitance between human body and sensor plate
- 2. Csg: capacitance between sensor plate and ground
- 3. Cbg: capacitance between human body and ground
- 4. Cse: capacitance between sensor plate and environment

The capacitance is also affected from environment humidity and temperature. In this case, Csb is function depend on the distance between the sensor and the human body which is the measurement interested.

1.3 Working modes for capacitive sensing

Considering the system shown in Figure 1.3, there are mainly three types of capacitive sensing working modes. Two of them involves the utilization of two sensors,



Figure 1.3: Capacitive sensing working modes from [7].

one for transmitting and the other one for receiving.

Transmit mode allows the human body to become part of the transmitter, with the body close to transmitter. This mode is suitable for close distance between the transmitter and the body, due to the significance of the capacitance between body and plate compared to the body ground capacitance.

Shunt mode happens when the body not very close to the plate, and the body ground capacitance prevails. In this situation, the signal received is decreased with the close of body.

The two methods are very powerful for the amount of information. For example, the transmit mode is adept at following the movement when the user is contact to the sensor. But according to the previous discussion, this would not be a simple and tag-less method.

Loading mode is simple because it needs only a single plate. It permits to measure the induced current in the plate. Both plate and body are referred to a common ground, that is a shared potential [7].

1.4 Previous research

There are many previous research done about human localization with capacitve sensing using different techniques. Some of them are [8], [9], [10]. Their work were to perform different types of methods and experiments to obtain the data which would be used to draw some conclusion.

The main idea about the circuit design is based on the measurement of signal amplitude and phase difference which is caused by the change in capacitance of a capacitive sensor. A copper plate of $16 \text{ cm} \times 16 \text{ cm}$ was chosen as a sensor. It has a

good sensitivity of 200 cm with a low noise and good resolution. With the sensor mounted on each wall of a room of $3 \text{ m} \times 3 \text{ m}$, human in this room was accurately localized.



Figure 1.4: Circuit design of front-end interface from [11].

As shown in Figure 1.4, the change in capacitance of the sensor is caused by human coupling. The board using DDS programmable signal generator to generate a sine wave of 10kHz. The sine signal is fed to two branches. Both of them have RC low pass filters but a sensor plate is used as a capacitor on one branch, which is shown as C_s in Figure 1.4. So that the sine signal passing through the RC filter is affected in terms of amplitude and phase. The high-pass filters that follow the reader circuits, C_1R_2 and C_2R_3 , eliminate the very low frequency noise and DC offsets from the buffered signals. Then sine signal of the upper branch is passed through a high Q-factor band-pass filters to remove most of the noise from the signal. The signals of the two front-ends paths, $V_{ref}(t)$ and $V_f(t)$ (see Fig 1.4), are then passed through a hysteresis comparator to prepare them for the phase discriminator circuit that is implemented using a XOR logic gate. The signals generated from the comparators are then passed through an XOR gate which generate $V_{out}(t)$. In order to obtain the DC component of the phase discriminator output, a 2nd-order low-pass filter is introduced which passes only the DC term. The DC value is measured by an internal ADC of ATmega328P microcontroller which is also used to control signal generator with SPI interface.

From [11], it explains the mathematical model. The two signals from the same signal source:

$$V_f(t) = Asin(2\pi f t + \theta), \qquad (1.4)$$

with $A = 3 V_{pp}$ and f = 10 kHz.

We model the noise captured by the sensor transducer, as an additive white Gaussian noise generator (AWGN) at the input of the reader circuit on the top path. So that at the output of the top path reader circuit we have:

$$V_{Sens}(t) = Bsin(2\pi ft + \theta + \phi) + N(0, N), \qquad (1.5)$$

while at the output of the bottom path read circuit we have:

$$V_{Ref}(t) = Asin(2\pi f t + \theta) \tag{1.6}$$

where the amplitude is

$$B = \frac{1}{\sqrt{1 + (2\pi f C_s R_1)^2}} \tag{1.7}$$

and the phase shift is

$$\phi = -\arctan(2\pi f C_s R_1) \tag{1.8}$$

The maximum phase shift sensitivity of an RC filter to variations of the filter capacitance is at the cut-off frequency. Hence adjust the value of R_1 in order to tune the R_1C_s cut-off frequency to 10 kHz, when nobody is present in sensor range. At the cut-off frequency we have

$$B = \frac{A}{\sqrt{2}} \tag{1.9}$$

and

$$\phi = -\frac{\pi}{4}.\tag{1.10}$$

So we have the formula for the R_1C_s at cut-off frequency:

$$V_{Sens}(t) = \frac{A}{\sqrt{2}} sin(2\pi ft + \theta - \frac{\pi}{4}) + N(0, N).$$
(1.11)

To redece the noise, a 4th order butterworth bandpass filter with a quality factor Q = 5 is implemented. Its gain is about 1.4, to compensate for the attenuation of R_1C_s filter:

$$V_f(t) = 1.4 \frac{A}{\sqrt{2}} \sin(2\pi f t + \theta - \frac{\pi}{4}).$$
(1.12)

The comparator hysteresis is small, the upper threshold voltage, V_{TH} , and lower threshold voltage, V_{TL} , are:

$$V_{TH} = V_{CC} \left(\frac{R_{11}}{R_{11} + R_{10}}\right),\tag{1.13}$$

$$V_{TL} = V_{EE} \left(\frac{R_{11}}{R_{11} + R_{10}}\right). \tag{1.14}$$

With $V_{CC} = 5$ V, $V_{EE} = 0$ V, $R_{10} = 100$ k Ω and $R_{11} = 1$ k Ω , we get $V_{TH} \simeq 50$ mV, $V_{TL} = 0$ V.

The fundamental frequency of both $V_1(t)$ and $V_2(t)$ is 10 kHz, so that the fundamental frequency of the output of the XOR gate, V_{out} , is double, $f_0 = 20$ kHz. The non-zero average signal V_{out} can be represented as a Fourier series.

$$V_{out} = \frac{5T_1}{T} + \sum_{k=1}^{\infty} (A_k \cos(2k\pi f_0 t) + B_k \sin(2k\pi f_0 t)), \qquad (1.15)$$

with

$$A_{k} = \frac{5}{k\pi} [sin(\frac{2\pi kT_{1}}{T})], \qquad (1.16)$$

$$B_k = \frac{5}{k\pi} [1 - \cos(\frac{2\pi kT_1}{T})]. \tag{1.17}$$

 T_1 depends directly on the variable phase shift introduced by the R_1C_s filter based on the value of the overall capacitance of the sensor plate which depends on the position of the human body.

After the 2nd-order lowpass filter, we get the DC term:

$$V_{out}(dc) = \frac{5T_1}{T}.$$
 (1.18)

After adjusting R_G to set the amplifier gain to G,

$$V_{dc} = G(V_{out}(dc) - V_R).$$
(1.19)

Thus, the output voltage V_{dc} changes with C_s , so that if the person approaches the sensor, the value when the person is out of the range of the sensor will be increased.

Chapter 2

The Project

2.1 Description

The circuit has been designed to measure the phase shift difference caused by an excitation signal and the response signal form the exited system. As shown on Figure 2.1, the microcontroller generates PWM signal with 10 kHz. One signal enters a low pass filter with R and C_s , where C_s is the capacitance of the sensor whose value depends on the distance between the plate and the human body. To achieve the maximum sensitivity of the RC filter, the R resistor should be adjustable in order to tune the RC filter at the cut-off frequency which is 10 kHz. The output signal from the RC filter and the original signal then are passed through two voltage follower. High pass filter after them eliminate the low frequency noise from the buffered signal.

To remove most of the noise, a narrow band-pass filter is used. The filter is only placed on the upper path of the sensor frontend and is implemented using a 4th order butterworth band-pass filter with a quality factor Q = 5.

The signals of the two frontend paths are then passed through a hystersis comparator to prepare them for the phase discriminator circuit that is implemented using a XOR gate. After the XOR gate, with a 2nd-order low-pass filter, the output is the DC term of the phase discriminator output. With no one in the range of the sensor, there is a phase difference between the two signals which is $\frac{\pi}{4}$. Thus we can obtain a static output value. To obtain V_{dc} , using a precision instrumentation amplifier AD623, and an adjustable resistor is implemented to set the instrumentation amplifier gain. The ATMAGA328p microcontroller samples the output from the amplifier. And then is used the XBEE to send the sampled data to the PC.



Figure 2.1: Block diagram of frontend interface.

2.2 Board testing

The board had already been checked by a previous researcher, so the function of the board basically worked. I started my work from the schematics and the datasheets of the IC components of the board. Ahead of all, I needed to understand every component and every section part. Since the previous researcher changed the board without modifying the schematic, firstly I modified the schematic to match the tested board, the layout is shown in Figure 2.2. Then I tested the board to verify it.

Firstly I tested the functionality of the board. My method was to run the board and verify the whole function of the circuit with digital multimeter, digital oscilloscope. I found the circuit is worked in correct behavior. Then I used the MATLAB to get the value through the wireless reception. The value changes with the human body movement. Before testing, I had to do a fine tuning to the circuit. I started from tuning the frequency of R_1C_s (R_1 is the adjustable resistor of the low-pass filter) at the cut-off frequency of 10 kHz, according to that the amplitude of the output of R_1C_s should be 0.707 of the original sinusoid when no one is in the range of the sensor plate. Another tuning is adjusting the trimmer implemented with the instrumentation amplifier to set the gain, so that the input of the ADC on The Project



Figure 2.2: Modified board PCB.

microcontroller is within range of ADC under all conditions. I have a MATLAB test code named "check sensitivity" to test the sensitivity of the board from the distance of 0 cm to the distance of 200 cm between human body and sensor plate. And another test code named "check stability" to test the stability of a long period running.

2.3 Problems

After the board testing, I found some problems through the plot of values. To check the sensitivity of the sensor, a person was asked to stay in front of the sensor with some predefined distances and the readings of V_{dc} were recorded by MATLAB. The man moved to the certain distances according to test scenario. The result is shown Figure 2.3. From the result, it shows the noise which is unwanted of more than 12 mV(12 mV/2.976 V=0.403%).



Figure 2.3: Result of sensitivity test of distance from 25 cm to 125 cm.

To check the stability, I kept the board running for several hours with no person in a room. Figure 2.4 shows a result of stability check. There is a huge drift of nearly 70 mV over 21 hours (70 mV/3.64 V=1.9%) which is very high.

2.4 Solutions to improve the front-end performance

Some drift could be caused by the environment changes such as temperature, humidity of the room, but these effects should be small. Then I found the 4th order butterworth band-pass filter (shown in Figure 2.5) also caused the drift. To solve this problem, I bypassed the BP filter and ran again the stability test. I found that





Figure 2.4: Result of stability for 21 hours.

the drift was reduced and is acceptable. The result is shown Figure 2.6.

The problem of noise could not be eliminated completely especially when the 4th order butterworth BP filter is bypassed. Firstly, I did some steps to reduce the noise in circuit. I reduced the length of the wire which connects circuit to the sensor plate. I used the trimmer TC33X-2-104E attached to board instead of using big size trimmer soldering on the board with wires. Also I resoldered some components on the board instead of using another board connected with wires.

Then I checked the power supplies. I used power supply with 5 V and 400 mA to power up the circuit, there was no any current limiting and voltage drop. There are 3 different voltage supplies on the board which are 10 V, 5 V (Virtual ground), and 3.3 V. Two components NCP1403 generate 10 V and 5 V. And the virtual ground 5 V is generated by a rail spiltter TLE2426. It can generate the voltage exactly in middle of 10 V and ground. ADP2503 generates the 3.3 V which is only for the power supply of XBEE part.



Figure 2.5: 4th order butterworth BP filter.

I noticed that some samples are missed when the data transmitted to the PC through the XBEE. With checking the power supply of XBEE, the voltage sometimes droped below 2V when loaded with XBEE part. So that XBEE would switch off if the voltage was not in its correct voltage supply. I checked both the datasheet of ADP2503 and the layout of board. I saw that the datasheet recommends to implement both input and output capacitor which should be very close to the component (as shown in Figure 2.7). While in layout, the input capacitor of ADP2503 was shared with 10 V regulator and 5 V regulator (as shown highlighted in Figure 2.8).

So that I soldered a 10uF capacitor very close to the regulato, then the problem was solved and the XBEE worked in correct transmission.

2.5 Component modification

The goal of the project is to design a small size and less power consumption circuit. So that I began to try finding some better components instead of the components now used.

Initially for the operation amplifier, we had a requirement that the amplifier should



Figure 2.6: Result of stability bypassing the butterwith BP filter.

be single or dual rail to rail, input impedance should be larger than 10 M Ω , power supply should over than 5 V, and slew rate should be more than 0.5 V/us. From the datasheet of OP184 which was now used, it basically achieves the requirement. But when considering the power consumption, its supply current is 1.45 mA which is quite large. So that I chose a rail to rail, low power op-amp OPA2990(As shown in Figure 2.9). It highly achieves the requirement, moreover, its supply current is only 120 uA which is a very small power consumption. Also it works well below 60 °C. Since OPA2990 consists of dual op-amp, we could save space on the PCB board.

For the comparator part, we need to achieve that power supply is larger than 5 V, slew rate is over than 1 V/us or 10 V/us. The comparator now used is implemented of op-amp which is not good because it could not achieve the full output. Here I chose the low power, rail to rail comparator MAX9142 (as shown in Figure 2.10). It is high speed whose propagation delay is 40 ns, while supply current is only 150 uA per comparator. It works well below 60 °C. Most importantly, its output stage



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Figure 2.7: Typical application circuit of AD2503.



Figure 2.8: Input capacitor shared within three regulators.

circuitry (as shown in Figure 2.11) is what we need. During an output transition, I_{SOURCE} or I_{SINK} is pushed or pulled to the output pin. The output source or sink current is high during the transition, creating a rapid slew rate. Once the output voltage reaches V_{OH} or V_{OL} , the source or sink current decreases to a small



Figure 2.9: Top view of OPA2990.

value, capable of maintaining the V_{OH} or V_{OL} static condition. This significant decrease in current conserves power after an output transition has occurred. One consequence of a current-driven output stage is a linear dependence between the slew rate and the load capacitance. A heavy capacitive load will slow down a voltage output transition. This can be useful in noise-sensitive applications where fast edges may cause interference.

Because of the change of some components, then I considered to redesign the power section part. According to the datasheets of the components I used, I need a 3.3 V voltage regulator for XBEE part, an analog voltage regulator of 5 V, a digital voltage regulator of 5 V, and a voltage reference whose output is half of the analog voltage regulator output.

I found a high-precision, dual-output series voltage reference MAX6072 (as shown in Figure 2.12). The MAX6072 is a dual-output precision series voltage reference. The product features two outputs, $+V_{REF}$ and $+\frac{V_{REF}}{2}$. The device exhibits a very low 1/f noise of 2 ppm (peak-to-peak). Each output can source and sink 10 mA and has an independent sense line. This product has a temperature drift of 6 ppm/°C (max) over the ambient temperature range of -40 °C to +125 °C and an initial accuracy of 0.04 %. And a pair of output voltages is 5 V/2.5 V which is available to my case. Also its supply current is low which is 150 uA.

For the digital voltage regulator and 3.3 V voltage regulator, to reduce the PCB size, I found a dual output, step-down charge pump with automatic mode





Figure 2.10: Top view of MAX9142.

switching, LTC3256 (as shown in Figure 2.13). The LTC3256 is a wide input range switched capacitor step-down DC/DC converter that produces two regulated outputs: a 5 V output via direct connection to the charge pump output, and a 3.3 V output via a low dropout (LDO) linear post-regulator. The device provides up to 350 mA of total output current. At 12 V V_{IN} and maximum load on both outputs, power dissipation is reduced by over 2 W compared to a dual LDO regulator solution. The LTC3256 has numerous safety features including overcurrent fault protection, overtemperature protection and tolerance of 38 V input transients.

Unfortunately, a problem occurs if we use the MAX6072. Because of the supply voltage of MAX6072 is 5.2 V to 5.5 V which is over the output voltage of power bank. Thus, I need design an extra external boost regulator. Here I chose XC6372 (as shown in Figure 2.14). It is a PWM controlled step-up DC/DC converters. The built-in 1.4 Ω switching transistor type enables a step-up circuit to be configured using only three components, a coil, a diode, and a capacitor. Output voltage can be selectable in the range from 2.0 V to 7.0 V in increments of 0.01 V (accuracy: $\pm 2.5 \%$).





Figure 2.11: Output stage circuitry of MAX9142.



Figure 2.12: Top view of MAX6072.

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Figure 2.13: Top view of LTC3256.



Figure 2.14: XC6372.

Chapter 3 The Results

3.1 Experimental setup

As shown in Figure 3.1, the sensor plate which is $16 \text{ cm} \times 16 \text{ cm}$ was mounted on a wall consists of boxes at a certain height. The board was put on the top of the box away from the power bank which would reduce the noise from the power bank. XBEE 802.15.4 RF modules by Digi International were used at both transmitting end and receiving end. And MATLAB was used to received the data on PC, then plot the data values.



Figure 3.1: Experimental setup.

3.2 Sensitivity

As shown in Figure 3.2 and Figure 3.3. The highest noise is around 4 mV (4 mV/2.604 V=0.154 %) with average noise 2 mV. The result is much better than the previous circuit. Comparing the results of these two tests, the noise is still visible which may be caused by person slightly movement during the test, electrical wiring in floor, etc. Moreover, if we just consider the value of certain distance, it could be considered that the circuit can detect a man up to 1.5 m.



Figure 3.2: Sensitivity test 1.

Table 3-1 shows a comparison of the previous front-end and the improved frontend in terms of sensitivity. SNR can be calculated as maximum change in voltage from 0.25 m to 1.5 m divided by mean amplitude of noise.



Figure 3.3: Sensitivity test 2.

Attribute	Previous Design	Improved Design
Peak noise	12 mV	4 mV
Average noise %	0.403~%	0.154~%
Max. Change in V	80 mV	20 mV
SNR	16.5	14.0
Range of detection	$1.25 \mathrm{~m}$	1.5 m

 Table 3.1: Comparison between the previous circuit and the improved circuit in terms of sensitivity

3.3 Stability

As shown in Figure 3.4 and Figure 3.5. To avoid other disturbance with the purpose only testing stability, the sensor plate was replaced by a fixed value of 18 uF capacitor. From the results, there was a small drift around 15 mV for 11 hours (1.36 mV/h and 15 mV/2.414 V=0.621 %), which may be caused by environment

affects such as humidity, temperature, etc. However, the stability of the circuit is better than before.



Figure 3.4: Stability test 1.

Table 3-2 shows a comparison of the previous front-end and the improved front-end in terms of stability.

Attribute	Previous Design	Improved Design
Max. Change in V	21 mV	17 mV
Drift in percentage	0.695~%	0.621 %

 Table 3.2: Comparison between the previous circuit and the improved circuit in terms of stability



The Results

Figure 3.5: Stability test 2.

3.4 PCB design

Figure C.4 shows the main board of the circuit and Figure C.5 shows the external voltage adaptor. The size of the board is reduced to $45 \text{ mm} \times 45 \text{ mm}$ which is less small than the previous board (65.5 mm $\times 75 \text{ mm}$).

Chapter 4 Conclusion and future work

With resoldering some part of the previous circuit, correcting several faults within the circuit and changing some components of the circuit, the results of the final circuit testing is achieved the goal of this project.

For future work, according to the new designed circuit layout, we could manufacture the PCB board and soldering the components on it to test. It should be better than it is now.
Appendix A

Microcontroller code

```
1 #define F_CPU 1600000UL
2
  #include <stdlib.h>
3
  #include <util/delay.h>
4
6 #include <stdint.h>
7 #include <avr/interrupt.h>
8 #include <avr/io.h>
9 #include <avr/pgmspace.h>
10 #include "USART.h"
11 #include "XBEE.h"
12 #include "XBEE_reception.h"
13
14 // Constants
15 //
16
17
18
19 // Serial port setup
20 #define BAUDRATE 9600
<sup>21</sup> #define BAUD_PRESCALLER (((F_CPU / (BAUDRATE * 16UL))) - 1)
22
23 // Server protocol
<sup>24</sup> #define SERVER_REQUEST_OFFSET 8
<sup>25</sup> #define SERVER_REQUEST_START_MEASUREMENT 49
<sup>26</sup> #define SERVER_ADDRESS 0xABCD
27
28 // Local variables
29 //
30 struct payload_s {
       uint32_t total_clocks_during_measurement;
31
```

```
uint32_t total_input_periods;
32
  } payload;
33
34
35 uint8_t *TX_payload = &payload;
  uint32_t total_clocks_during_measurement = 0;
36
  uint32_t total_input_periods = 0;
37
  //float input_signal_frequency = 0.0;
38
39
40
41
42
43 const int sinewave_length=20;
  volatile uint8_t end_isr ;
44
  volatile uint16_t sample;
45
46 volatile uint16_t sample;
  char from_slave;
47
48
  const uint8_t sinewave_data[] PROGMEM =
      \{20, 23, 26, 29, 32, 34, 36, 38, 39, 40, 40, 40, 39, 38, 36, 34, 32, 29, 26, 23, 20,
49 17, 14, 11, 8, 6, 4, 2, 1, 0, 0, 0, 1, 2, 4, 6, 8, 11, 14, 17;
50 uint32_t total;
 //uint8_t LowBit;
51
52 //uint8_t HighBit;
_{53} // const uint8_t sinewave_data [] PROGMEM = {20, 26, 32, 36, 39, 40,
      39, 36, 32, 26, 20, 14, 8, 4, 1, 0, 1, 4, 8, 14;
54
55
  ISR(TIMER0_COMPA_vect) {
56
       if (sample >= sinewave_length) {
57
           sample = -1;
58
      }
       else {
60
           OCR1A = pgm_read_byte(&sinewave_data[sample]);
61
      }
62
  ++sample;
  //end_{isr=1};
64
65
66
67
68
69
void setPrescaler(uint8_t prescaler)
  {
71
      uint8_t mask = 0XF8;
72
      ADCSRA &=mask;
73
      ADCSRA = prescaler;
74
75
76
  }
77
 void adc_init()
78 {
```

```
setPrescaler(4);
79
       ADMUX \mid = (1 < < \text{REFS0});
80
       ADMUX &= \sim (1 < < \text{REFS1});
81
       ADCSRA \mid = (1 < < ADEN);
                                                  //Power up the ADC
82
83
   }
84
85
   void adc_start()
86
87
   {
       ADCSRA \mid = (1 < < ADSC); // star convertion
88
   }
89
90
91
   int main(void)
92
93
   ł
94
       DDRB \mid = (1 \iff DDB1) \mid (1 \iff DDB2);
95
       // PB1 and PB2 is now an output
96
       ICR1 = 40;
97
       // set TOP to 16 bit
98
99
       OCR1A = pgm_read_byte(\&sinewave_data[0]);;
100
       // set PWM for 25% duty cycle @ 16bit
102
       TCCR1A |= (1 \ll \text{COM1A1}) | (1 \ll \text{COM1B1});
103
       // set none-inverting mode
104
       TCCR1A \mid = (1 \iff WGM11);
106
       TCCR1B |= (1 << WGM12) | (1 << WGM13);
       // set Fast PWM mode using ICR1 as TOP
108
       TCCR1B |= (1 << CS10);
       // START the timer with no prescaler
111
       //setting TIMER0 for updating the values for PWM period
113
114
       // Set up Timer 0 to send a sample every interrupt.
115
       cli(); // disable interrupts
116
       // Set CTC mode (Section 15.9.2 Clear Timer on Compare Match)
117
       // WGM = 0b0100, TOP = OCR1A, Update 0CR1A Immediate (Table 15-4)
118
       // Have to set OCR1A *after*, otherwise it gets reset to 0!
119
       TCCR0A = 0:
120
       TCCR0B = 0;
121
122
       TCCR0B \mid = (1 \ll WGM02);
       TCCR0A \mid = ((1 \iff WGM01) \mid (1 \iff WGM00));
124
125
       // No prescaler, CS = 0b001 (Table 15-5)
126
127
       TCCR0B = (TCCR0B \& \sim (BV(CS02) | BV(CS01))) | BV(CS00);
```

```
// Set the compare register (OCR1A).
128
       // OCR1A is a 16-bit register, so we have to do this with
       // interrupts disabled to be safe.
130
                                                // 16e6 / 8000 = 2000
       OCR0A = 10; //F_CPU / SAMPLE_RATE;
       // Enable interrupt when TCNT1 = OCR1A (p.136)
132
133
       TIMSK0 \mid = \_BV(OCIE0A);
134
       sample = 0;
       sei(); // enable interrupts
136
       adc_init();
137
       XbeeUSART_init();
138
       adc_start(); //start the first conversion and ignore it
139
       while (ADCSRA & (1 << ADSC));
140
       while (1)
141
       {
142
            //\_delay\_ms(10);
143
            //sum all measurement
144
            total = 0;
145
            for (int i=0; i < 4096; i++)
146
            {
147
148
                adc_start();
149
                while (ADCSRA & (1 << ADSC));
150
                total +=ADC;
151
            }
152
            payload.total_clocks_during_measurement = total;//
154
      total\_clocks\_during\_measurement;
            payload.total_input_periods = 1234;//total_input_periods;
155
      THIS IS DUMMY
156
            XBee_TX_Request(SERVER_ADDRESS, TX_payload, sizeof(struct
157
      payload_s));
158
            // we have a working Fast PWM
159
       }
160
161
  }
```

Appendix B

Server codes

Check sensitivity

```
%%%%% External functions used in this script %%%%%
2 % send_API_2.m --> sends request to gather frequency readings
3 % rec_API_2.m -> receives freuency readings from cap-sensor nodes
4 % send_US_req.m --> sends request for Ultrasound sensor's readings
5 clear all;
6 close all;
7 clc;
8 %fclose(instrfind);
10 minutes_of_run = 1;
                        %each sub-run
11 hours = 6;
                          %total run
12
13 serialPort = 'COM4';
_{14} s = serial(serialPort);
<sup>15</sup> set (s, 'BaudRate', 9600);
16
 17
18
19 data_received = zeros(200000,1);
20
21 fopen(s);
22
_{23} start_time = clock;
24 flushinput(s);
<sup>25</sup> flushoutput(s);
26
27 data_index = 1 ; %array index to store US/cap sensor values
_{28} i = 1;
<sup>29</sup> pause (1);
```

```
_{30} end_at = 60*minutes_of_run;
_{31} j=0;
32
  while j\ <\ hours
33
  t0 = clock;
34
35
  while \operatorname{etime}(\operatorname{clock}, t0) < \operatorname{end}_at
36
37
       if (s.BytesAvailable > 16)
38
39
40
            [address, total_clocks_during_measurement,
41
      total_input_periods] = rec_API_2b(s);
42
                if (total_clocks_during_measurement > 0)
43
44
                 data_received(i) =(double(total_clocks_during_measurement
45
      )/64.0) * (5.0/65536.0);
46
                 disp(data_received(i));
47
48
                 i=i+1;
49
50
                end
51
        end
52
  end
53
54
  t0=clock;
55
56
  j=j+1;
57
58
<sup>59</sup> data=nonzeros(data_received);
  save(sprintf('frontend2_%s.mat', datestr(now, 'mm-dd HH-MM')), 'data');
60
61
  end
62
63
64 data=nonzeros(data_received);
65 save(sprintf('frontend2_%s.mat', datestr(now, 'mm-dd HH-MM'))), 'data');
66
67 figure (1);
68 plot (data);
69 ylabel('voltage [V]');
70 title('sensitivity');
  grid on;
71
72
73 xticks (1:(length(data)/6):length(data)+1);
74 xlabel('Distance[m]');
<sup>75</sup> xticklabels ({ '0.25 ', '0.50 ', '0.75 ', '1.00 ', '1.50 ', '2.00 '});
```

Check stability

```
1 %%%%% External functions used in this script %%%%%
2 % send_API_2.m --> sends request to gather frequency readings
3 % rec_API_2.m -> receives freuency readings from cap-sensor nodes
4 % send_US_req.m --> sends request for Ultrasound sensor's readings
5 clear all;
6 close all;
  clc:
7
8 % fclose (instrfind);
9
                          %each sub-run
10 minutes of run = 60;
11 %sub_hours = 1;
                           %total run
12 hours = 12;
13
  serialPort = 'COM4';
|s| = serial(serialPort);
<sup>16</sup> set (s, 'BaudRate', 9600);
17
19
20 data_received = zeros (200000,1);
21
22 fopen(s);
23
_{24} start_time = clock;
25 flushinput(s);
26 flushoutput(s);
27
28 data index = 1 ; %array index to store US/cap sensor values
 i = 1;
29
30 pause (1);
31 end_at = 60*minutes_of_run;
_{32} j=0;
33
_{34} while j < hours
t0 = clock;
36
  while etime(clock, t0) < end_at
37
38
      if (s.BytesAvailable > 16)
39
40
41
```

```
[address, total_clocks_during_measurement,
42
      total_input_periods] = rec_API_2b(s);
43
                if (total_clocks_during_measurement > 0)
44
45
                 data_received(i) =(double(total_clocks_during_measurement
46
      )/64.0) * (5.0/65536.0);
47
                 disp(data_received(i));
48
49
                 i=i+1;
50
51
                end
52
        end
53
  end
54
55
56
  t0=clock;
57
  j=j+1;
58
59
  data=nonzeros(data_received);
60
  save(sprintf('frontend2_%s.mat', datestr(now, 'mm-dd HH-MM')), 'data');
61
62
  end
63
64
65 data=nonzeros(data received);
  save(sprintf('frontend2_%s.mat', datestr(now, 'mm-dd HH-MM')), 'data');
66
67
  figure(1);
68
  plot(data);
69
70 ylabel('voltage [V]');
71 title('stability');
72 grid on;
73 xticks (1:(length(data)/11):length(data)+1);
\begin{array}{c} & \text{rabel}(`'\text{Time}[h]'); \\ & \text{rsticklabels}(\{`0', '1', '2', '3', '4', '5', '6', '7', '8', '9', '10'\}); \\ \end{array}
  saveas(gcf, sprintf('frontend2_FINAL_stability_start_17_30_%s.png',
76
      datestr(now, 'mm-dd HH-MM')));
```

Appendix C PCB design



Figure C.1: Schematic of main circuit



Figure C.2: Schematic of power supply



EXTERNAL BOOST REGULATOR

Figure C.3: Schematic of external boost circuit



Figure C.4: Layout of main board

PCB design



Figure C.5: Layout of external boost circuit

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